

FIG. 2

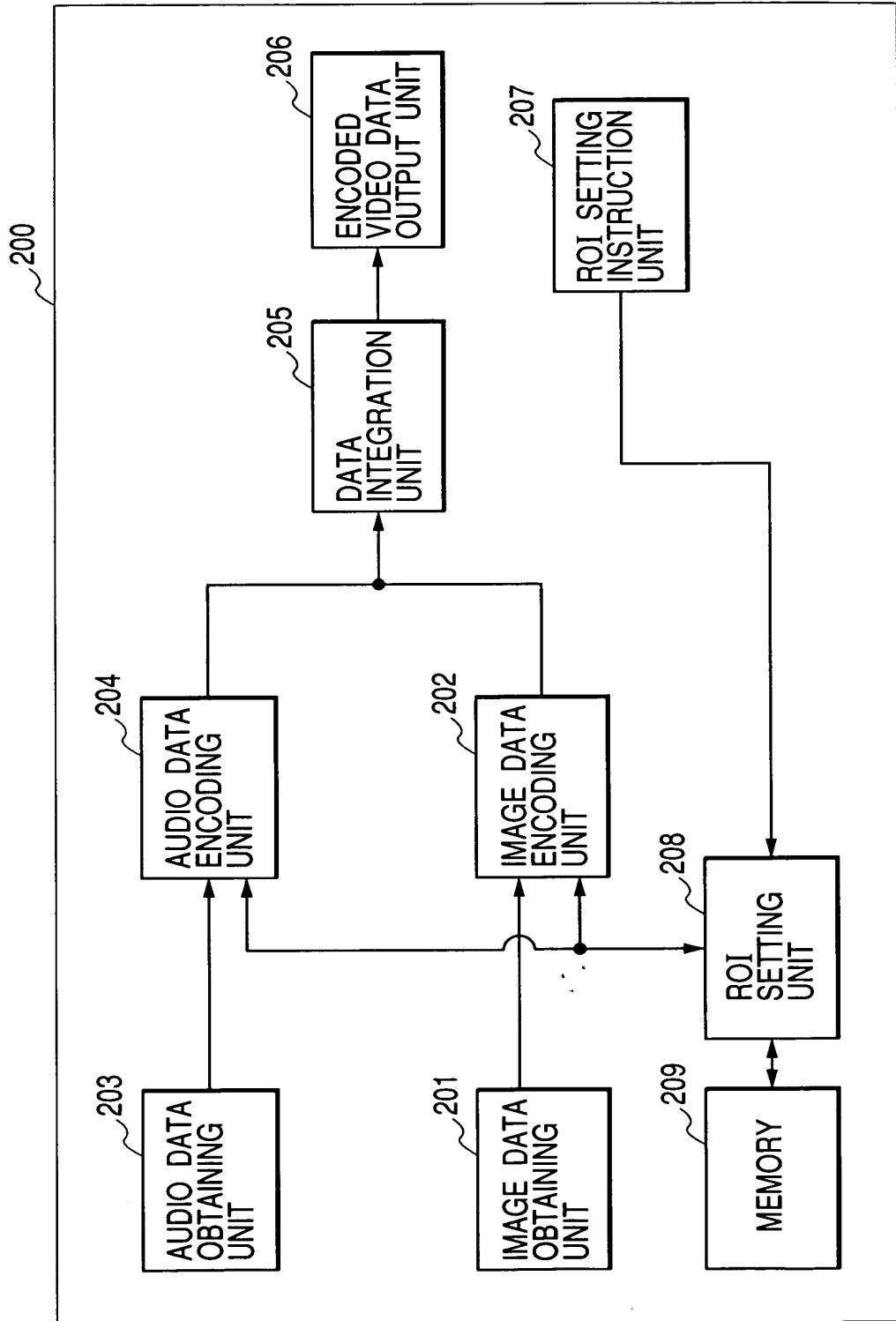


FIG. 3

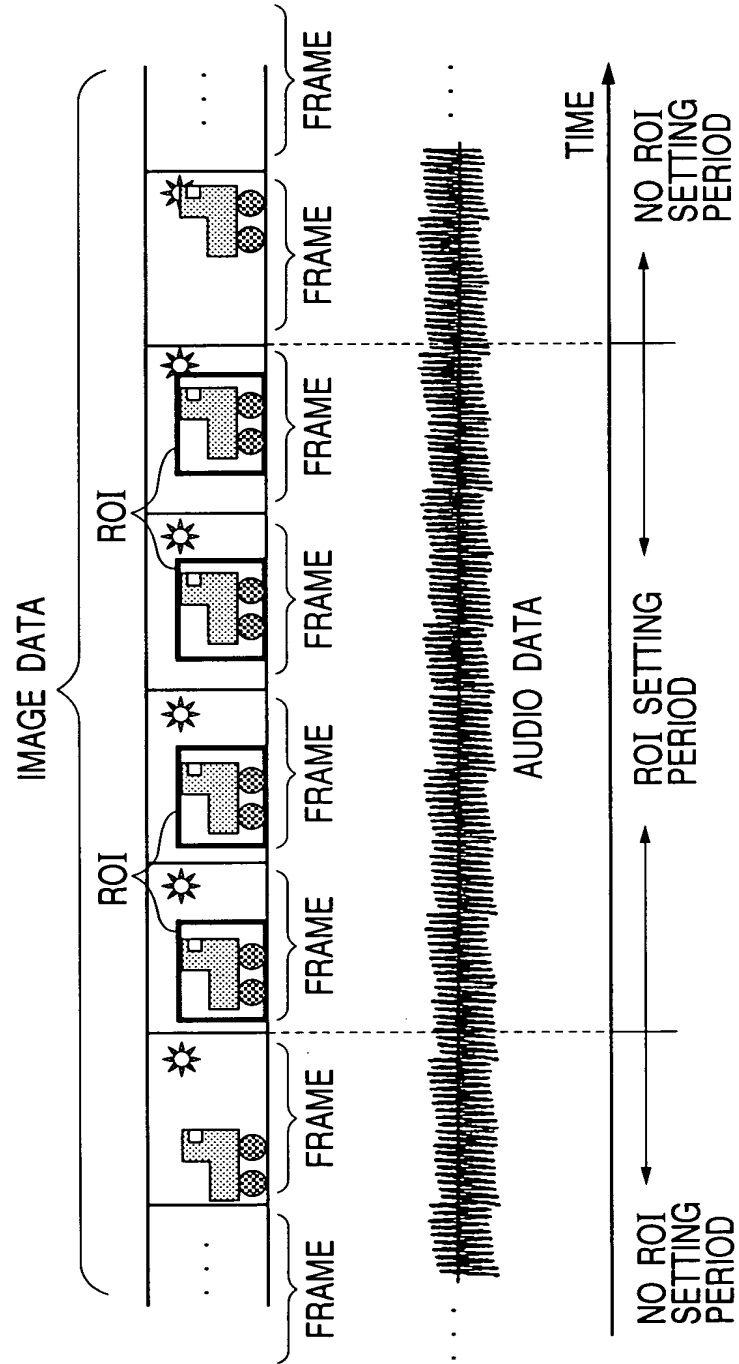


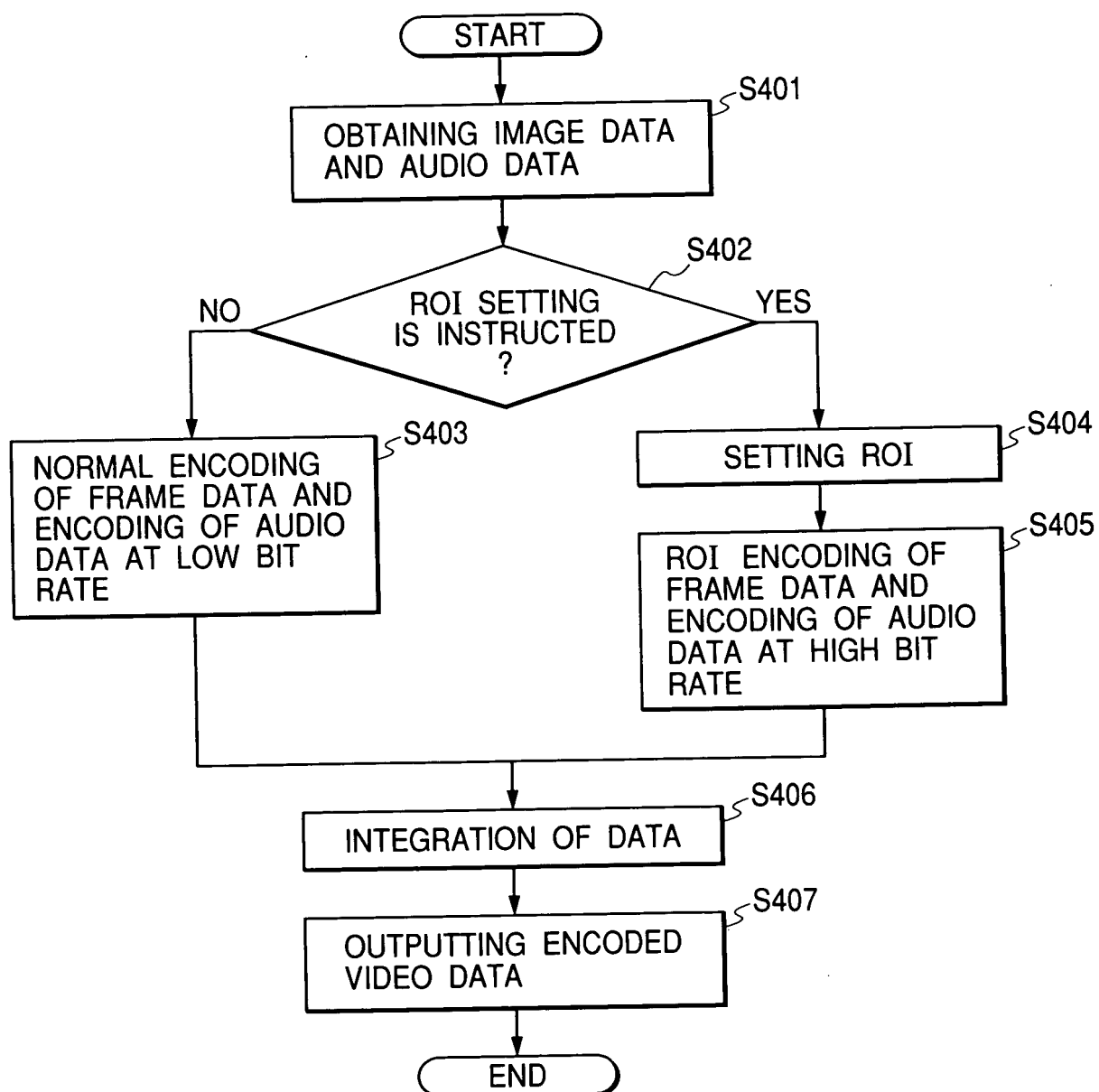
FIG. 4

FIG. 5

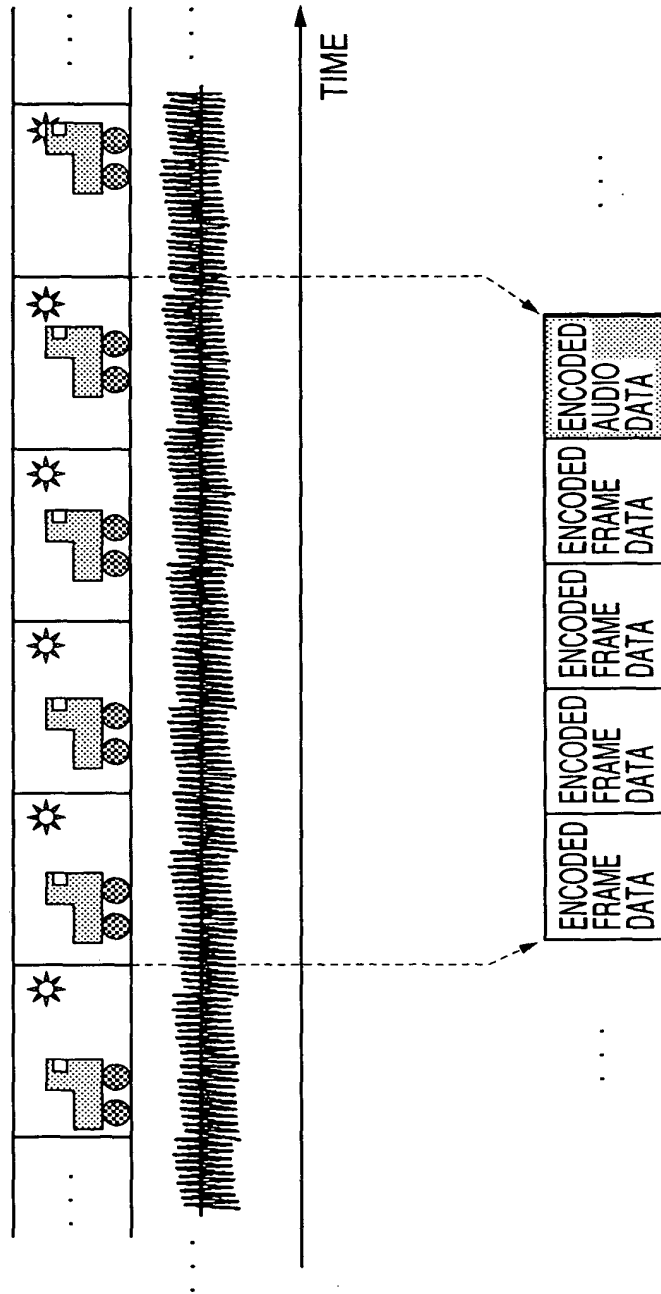


FIG. 6

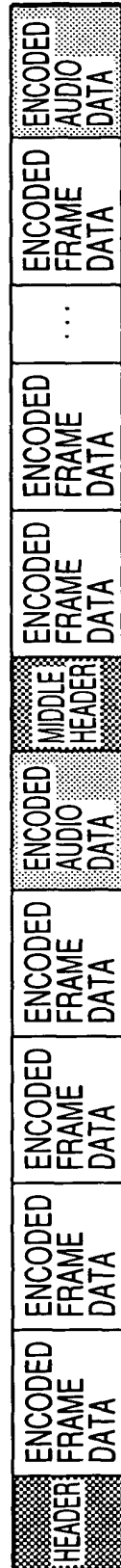


FIG. 7

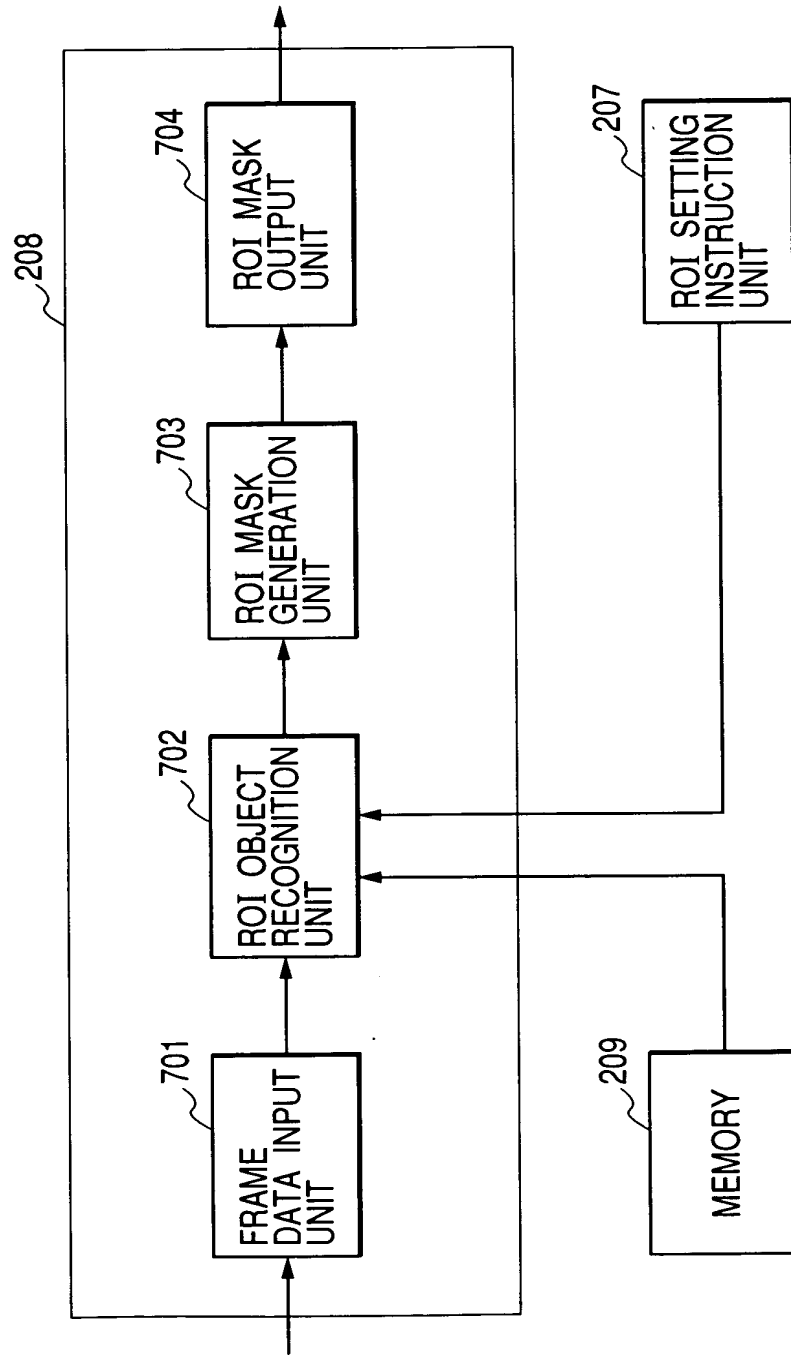


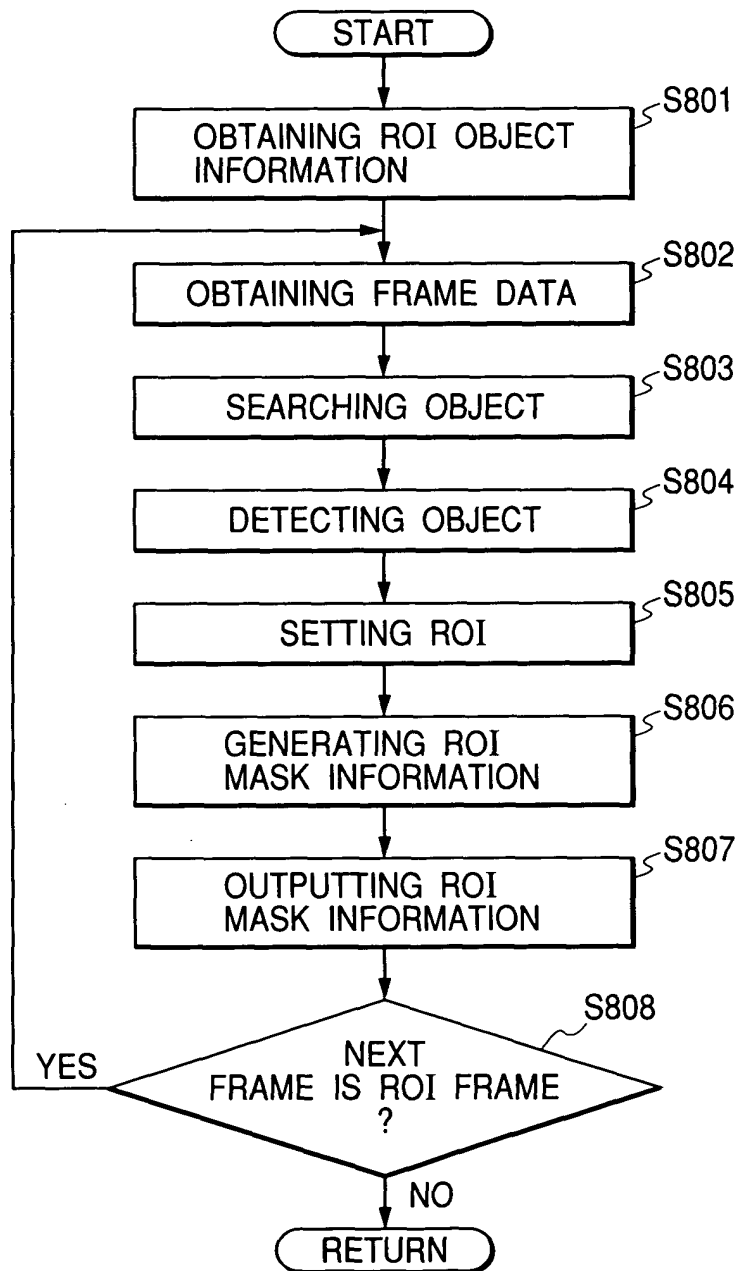
FIG. 8

FIG. 9

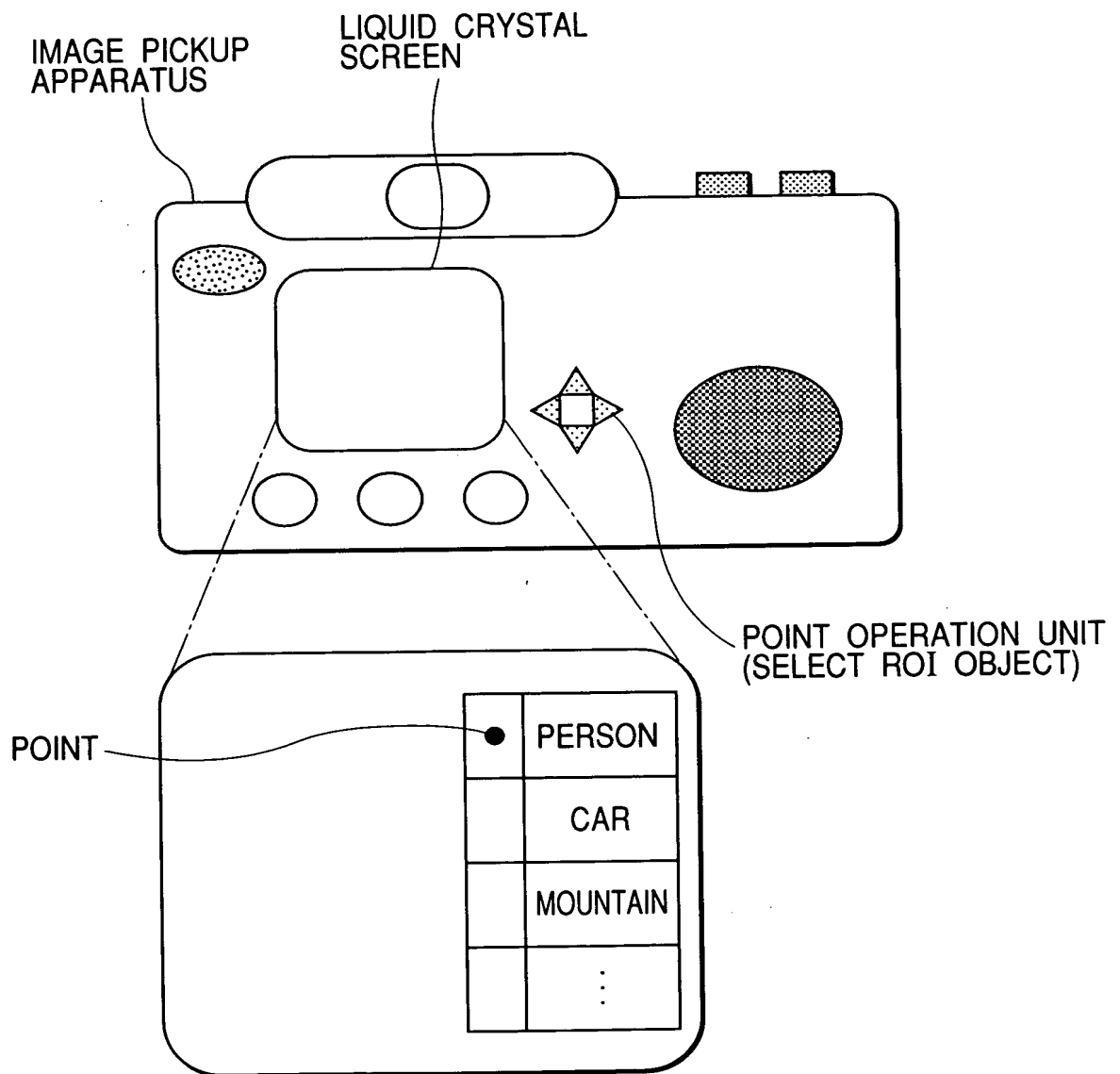


FIG. 10

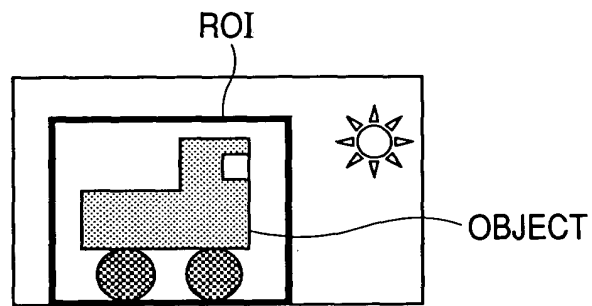
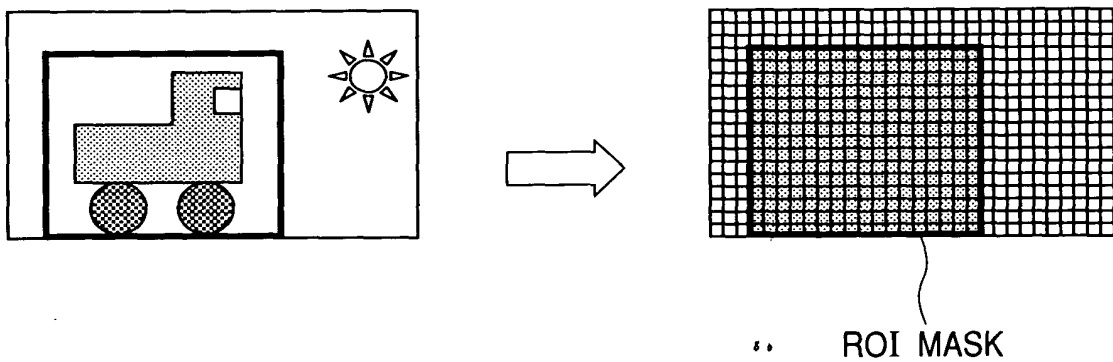


FIG. 11



BIT "1" IS ASSIGNED TO
A PIXEL OF GRAY, AND
BIT "0" IS ASSIGNED TO
A PIXEL OF WHITE

FIG. 12

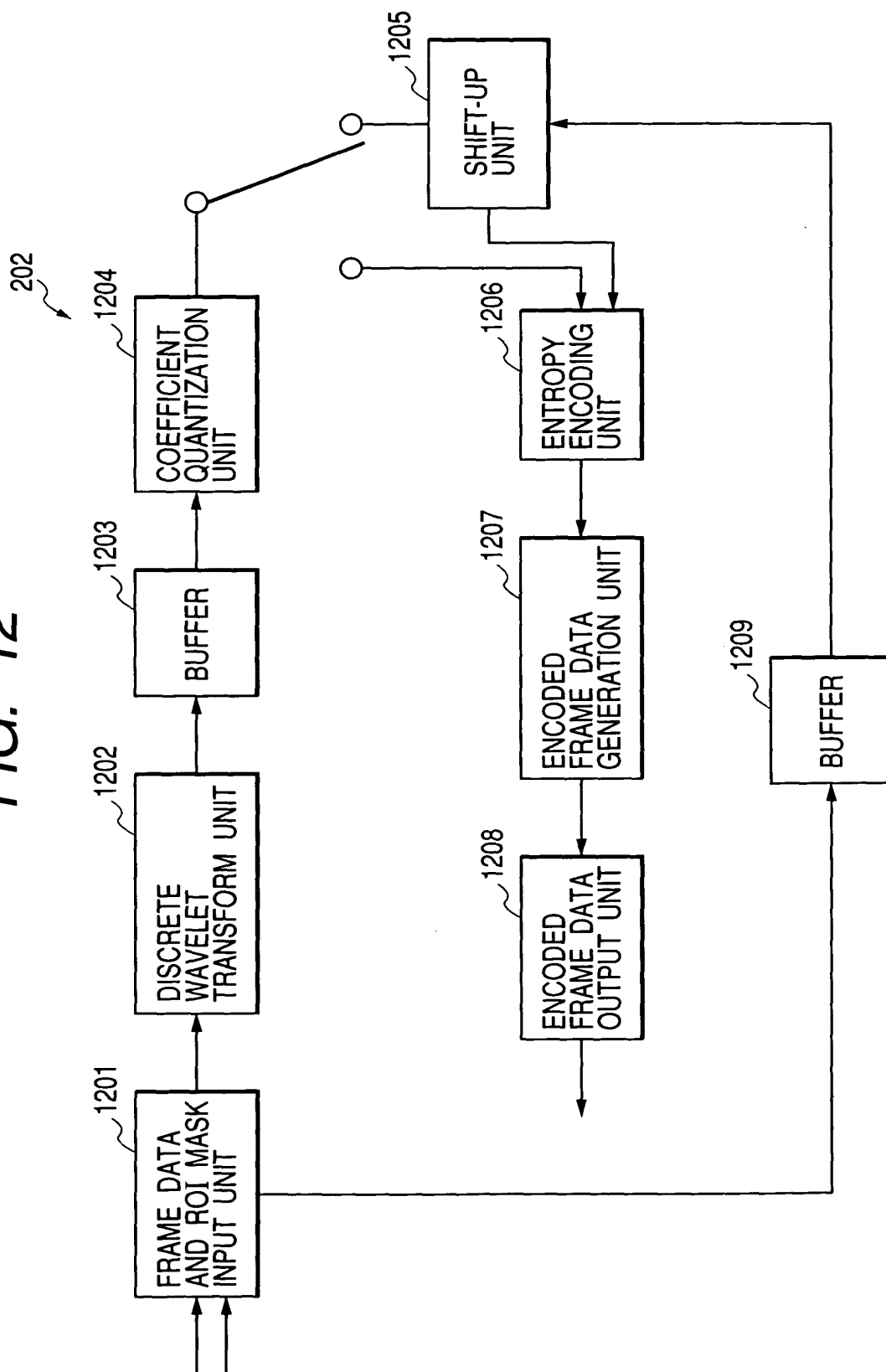


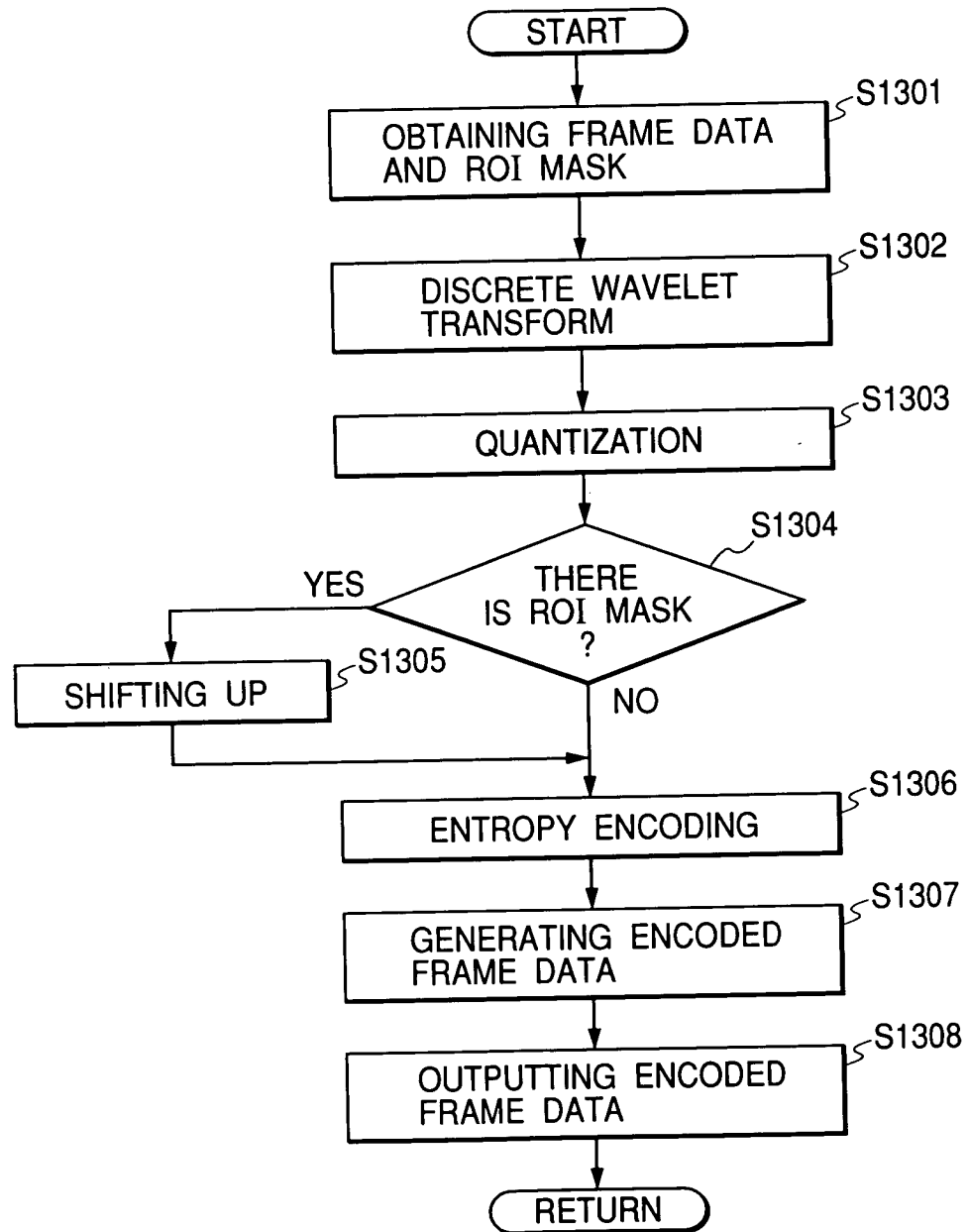
FIG. 13

FIG. 14

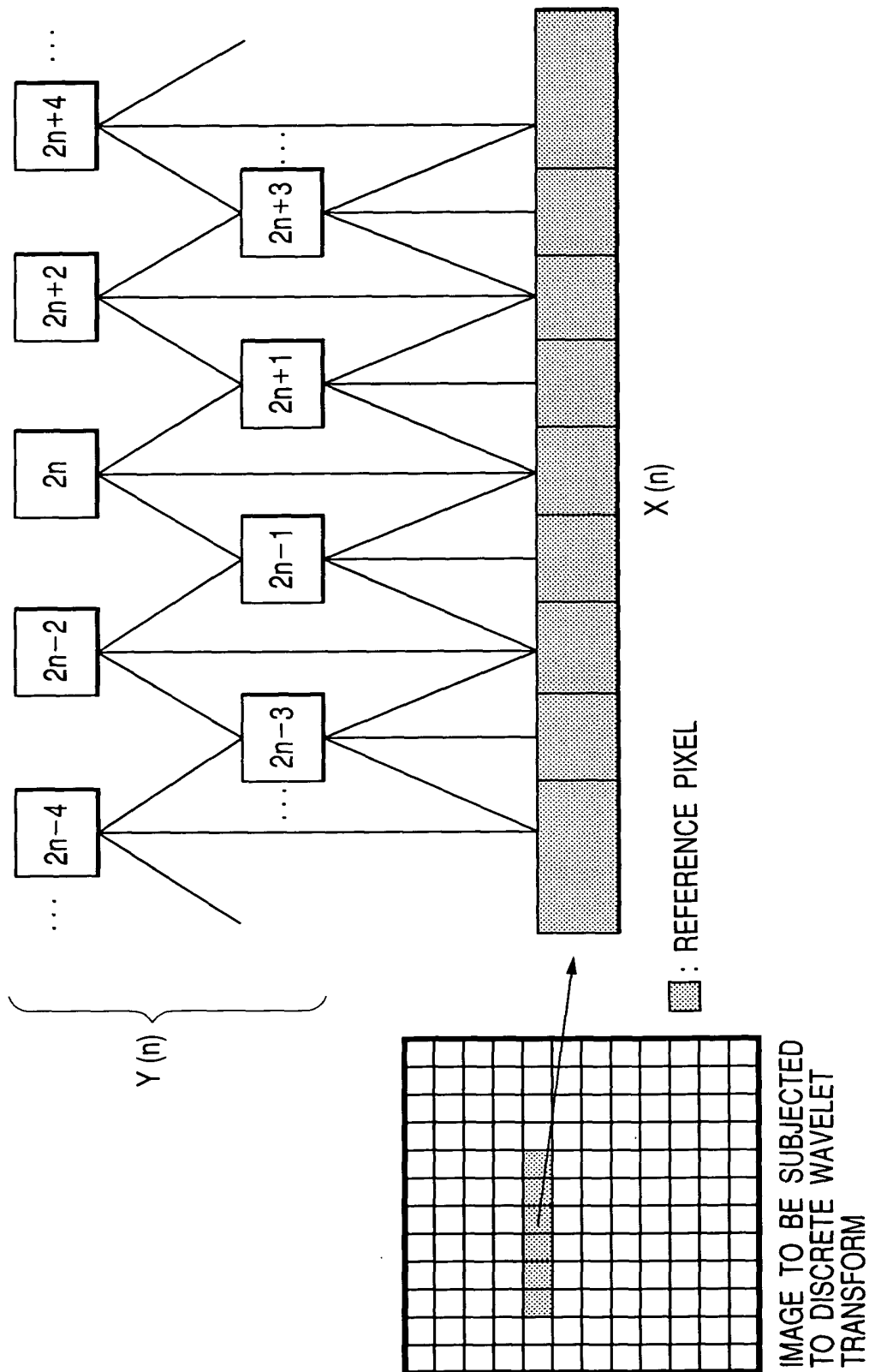


FIG. 15A

LL	HL
LH	HH

FIG. 15B

LL	HL	HL
LH	HH	HH
LH		HH

FIG. 15C

LL	HL1	HL2	HL3
LH1	HH1	HH2	HH3
LH2		LH3	HH3

LEVEL 0: LL
 LEVEL 1: HL1, HH1, LH1
 LEVEL 2: HL2, HH2, LH2
 LEVEL 3: HL3, HH3, LH3

FIG. 16

FREQUENCY COMPONENT	QUANTIZATION STEP
LL	1
HL1	2
HH1	2
LH1	2
HL2	4
HH2	4
LH2	4
HL3	8
HH3	8
LH3	8

FIG. 17A

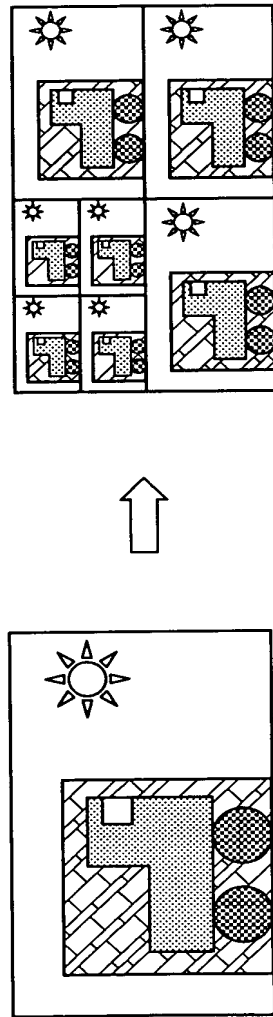


FIG. 17C

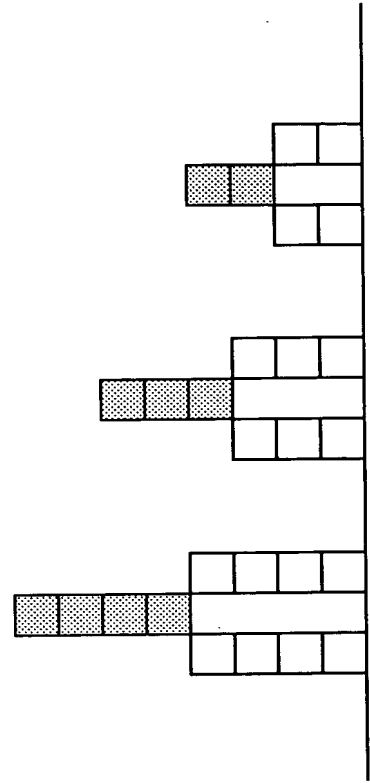


FIG. 17B

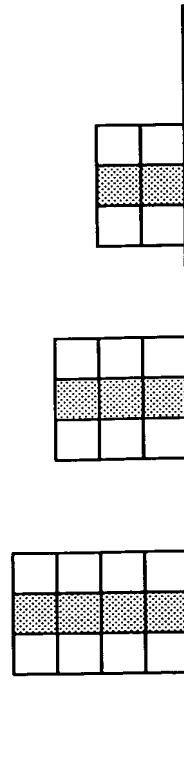


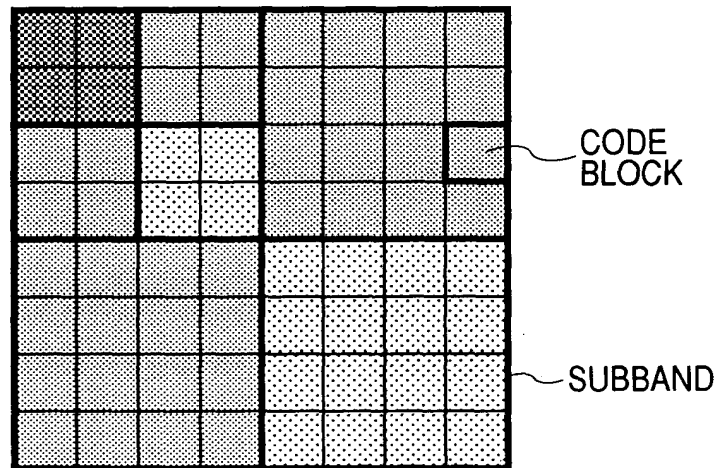
FIG. 18

FIG. 19

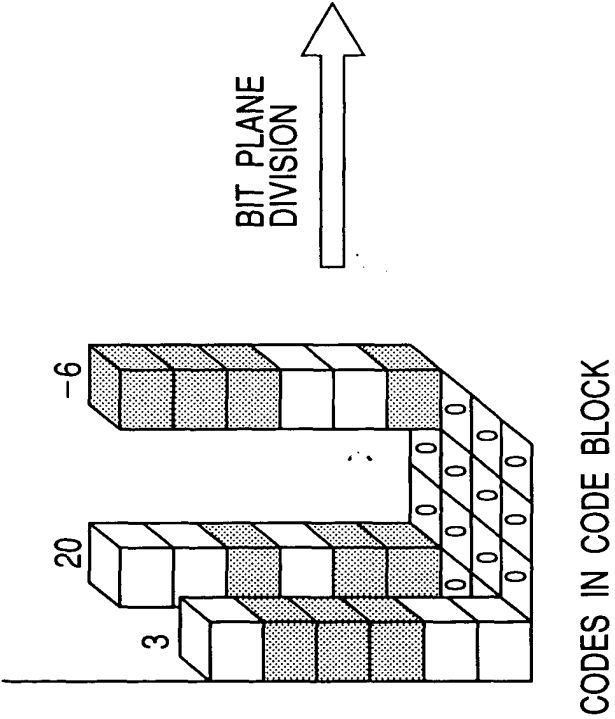
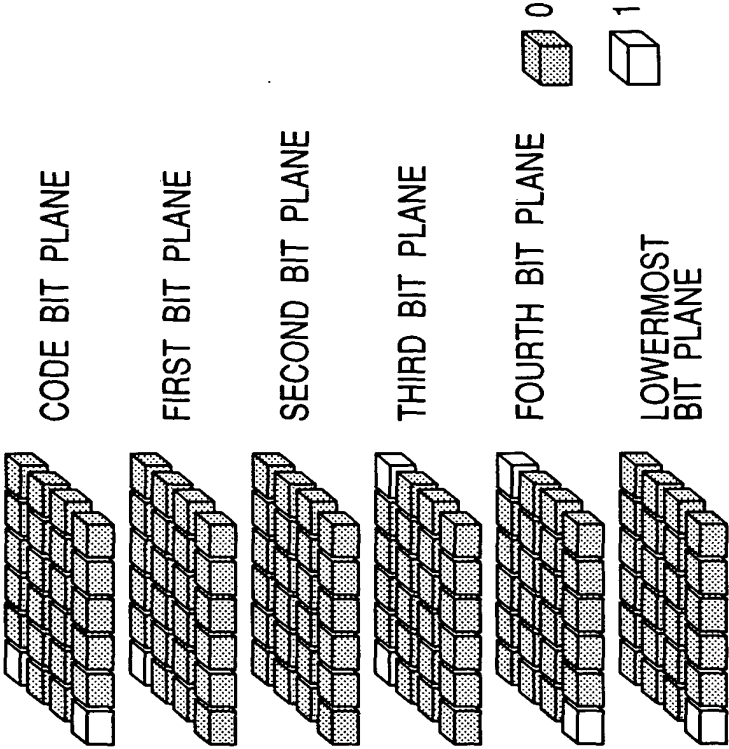


FIG. 20

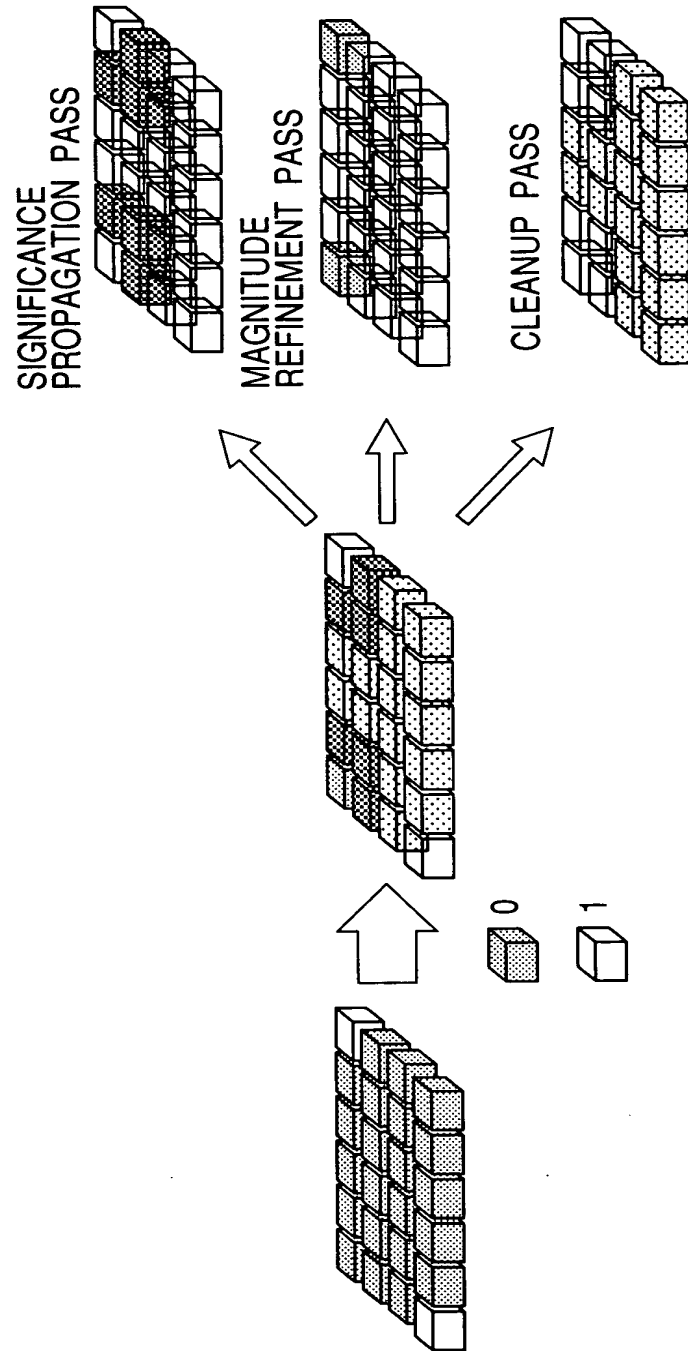


FIG. 21

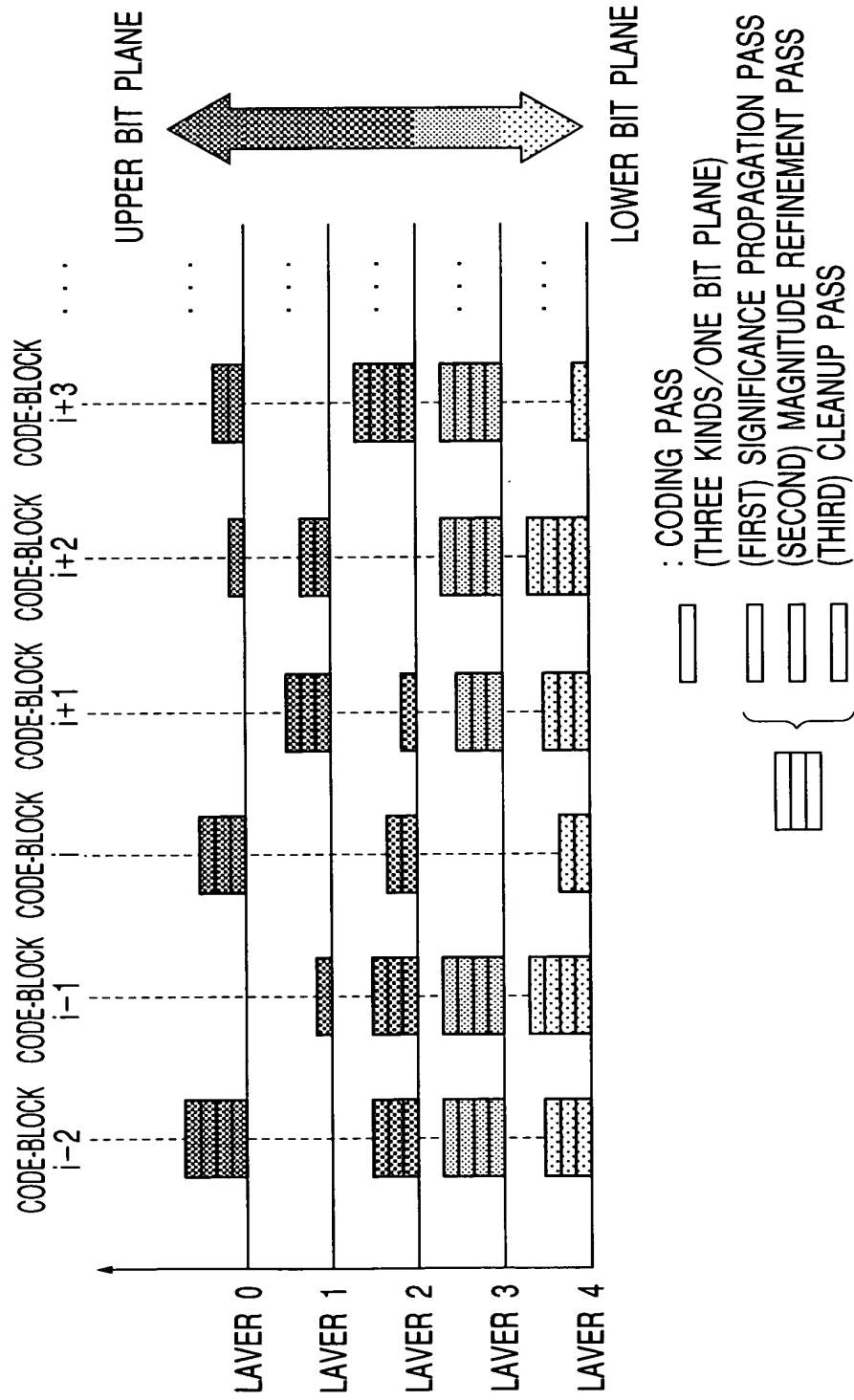


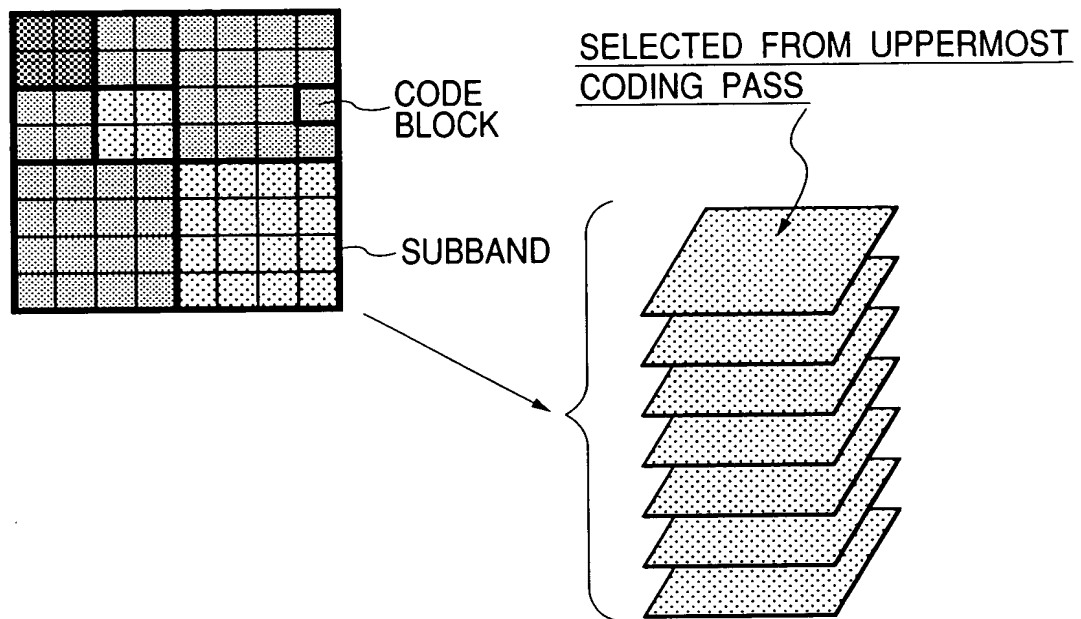
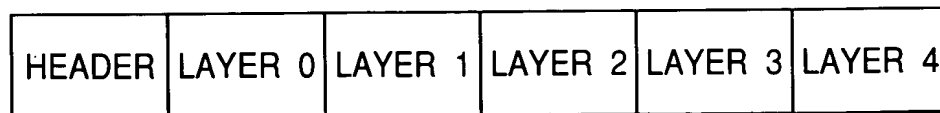
FIG. 22*FIG. 23*

FIG. 24

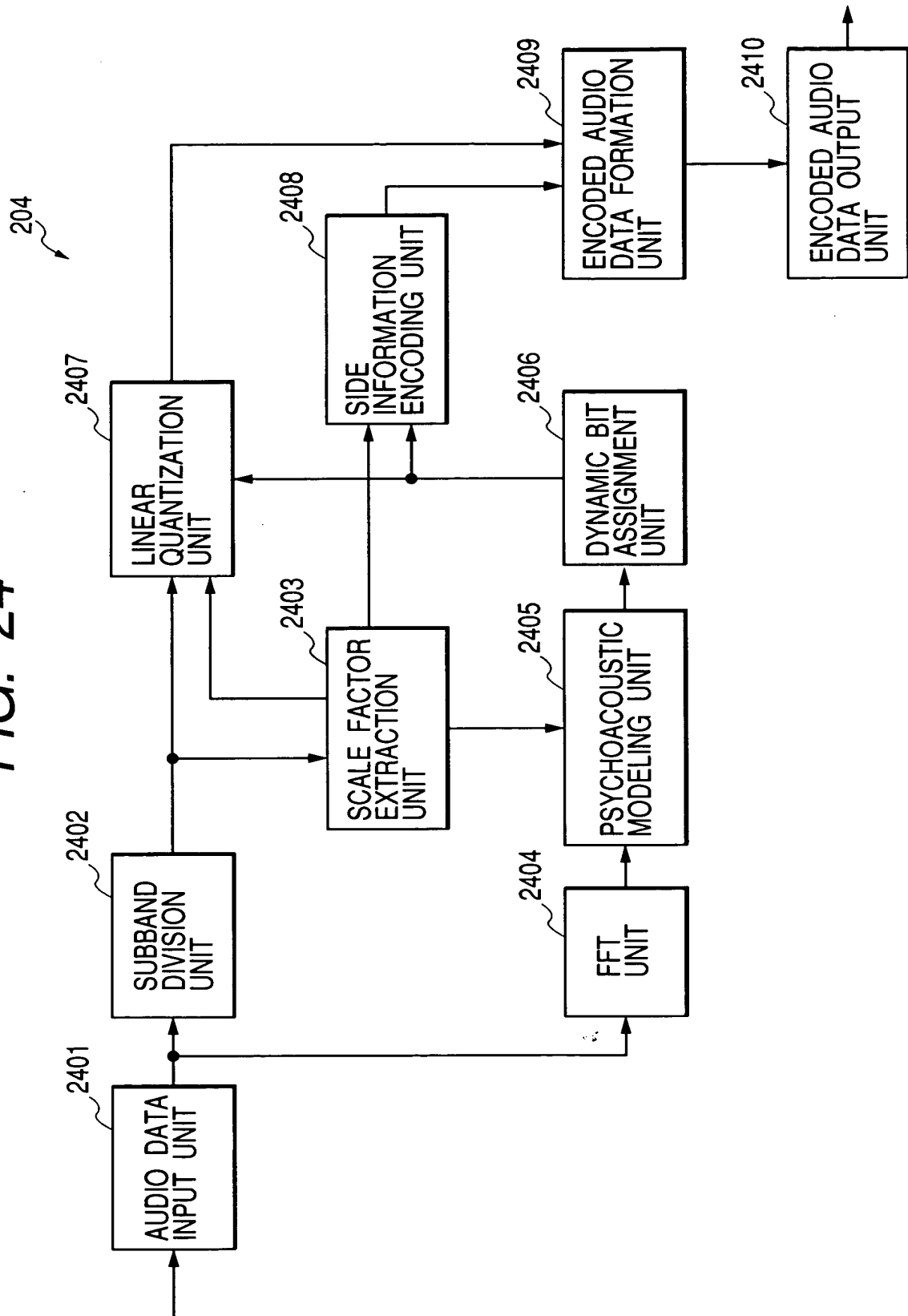


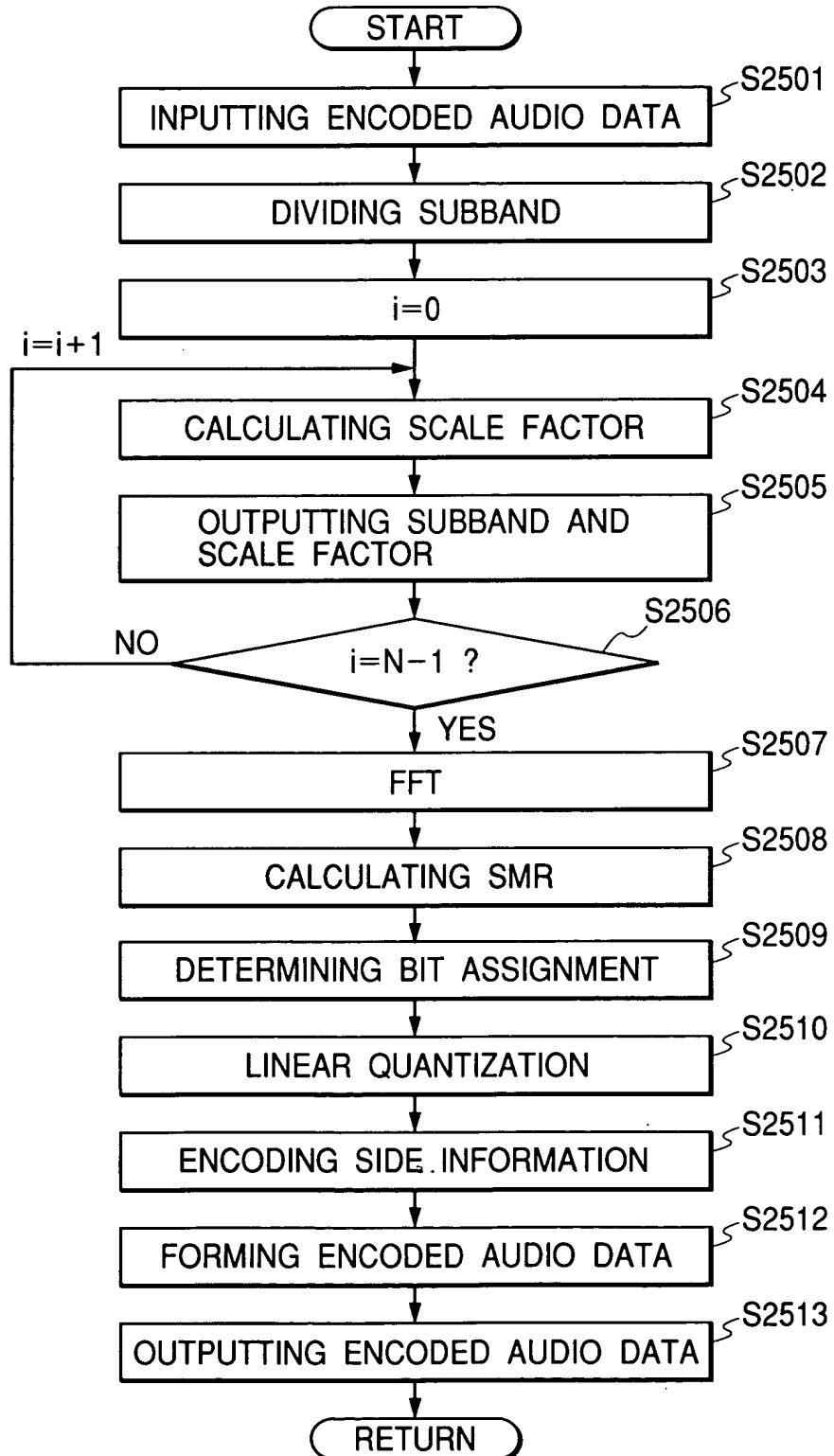
FIG. 25

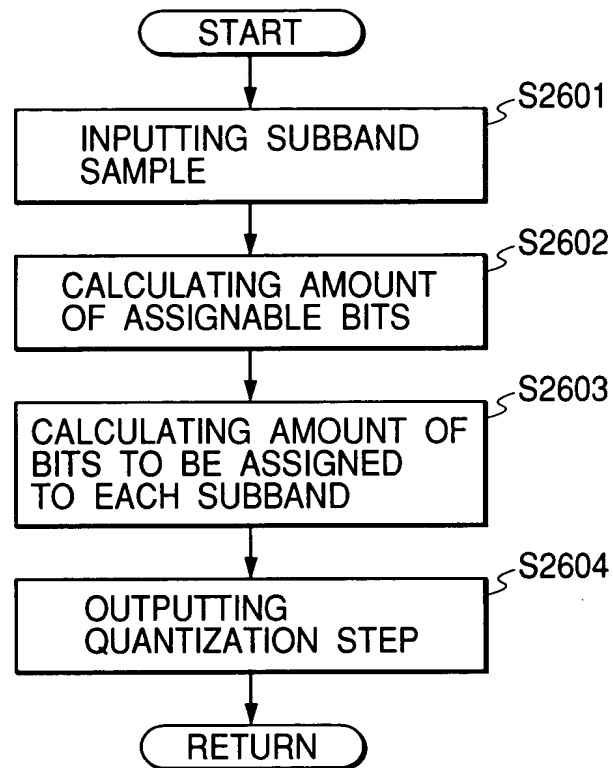
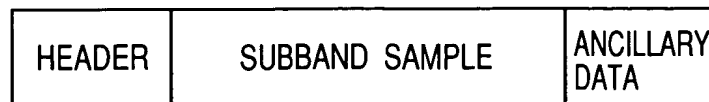
FIG. 26**FIG. 27**

FIG. 28

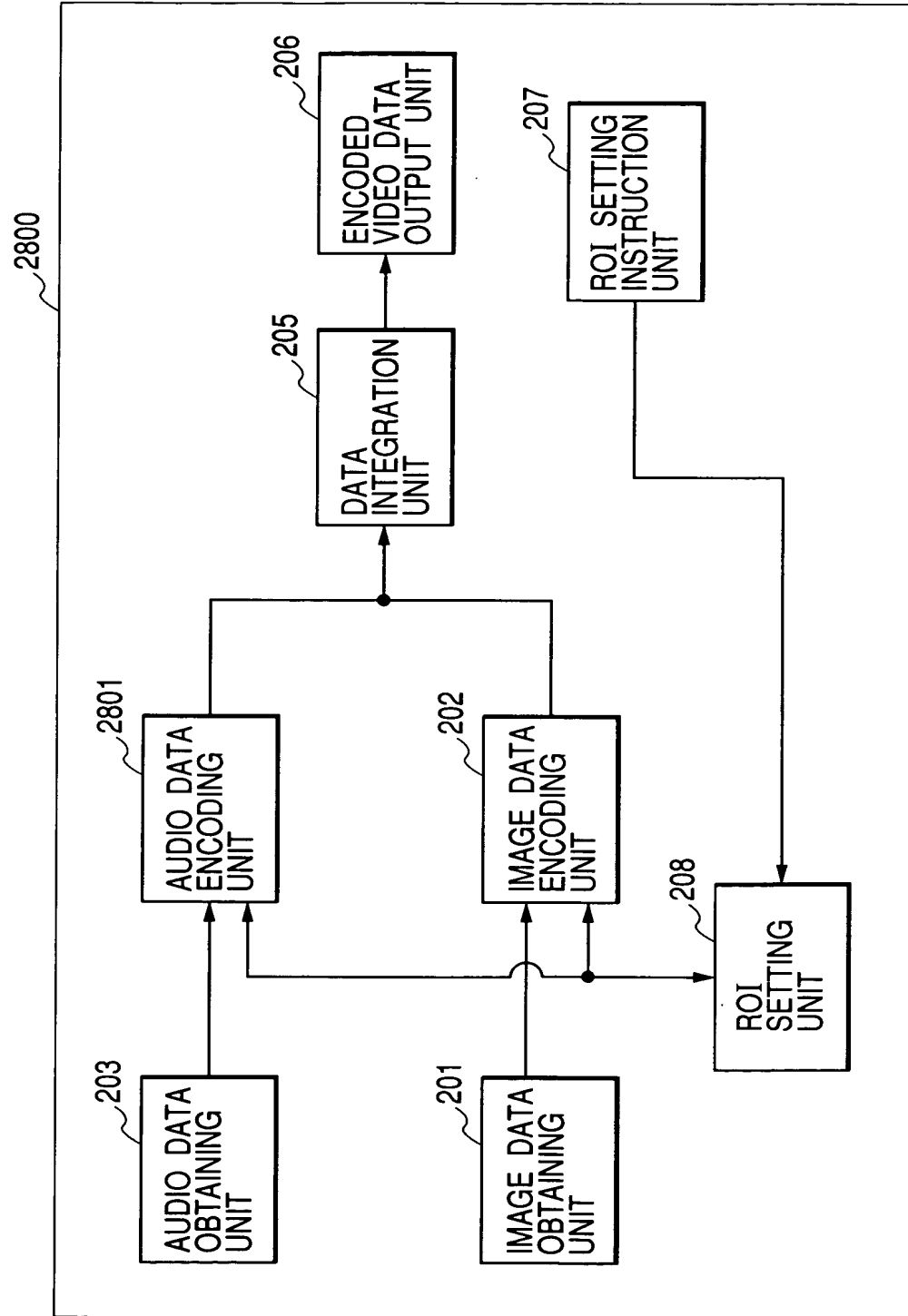


FIG. 29

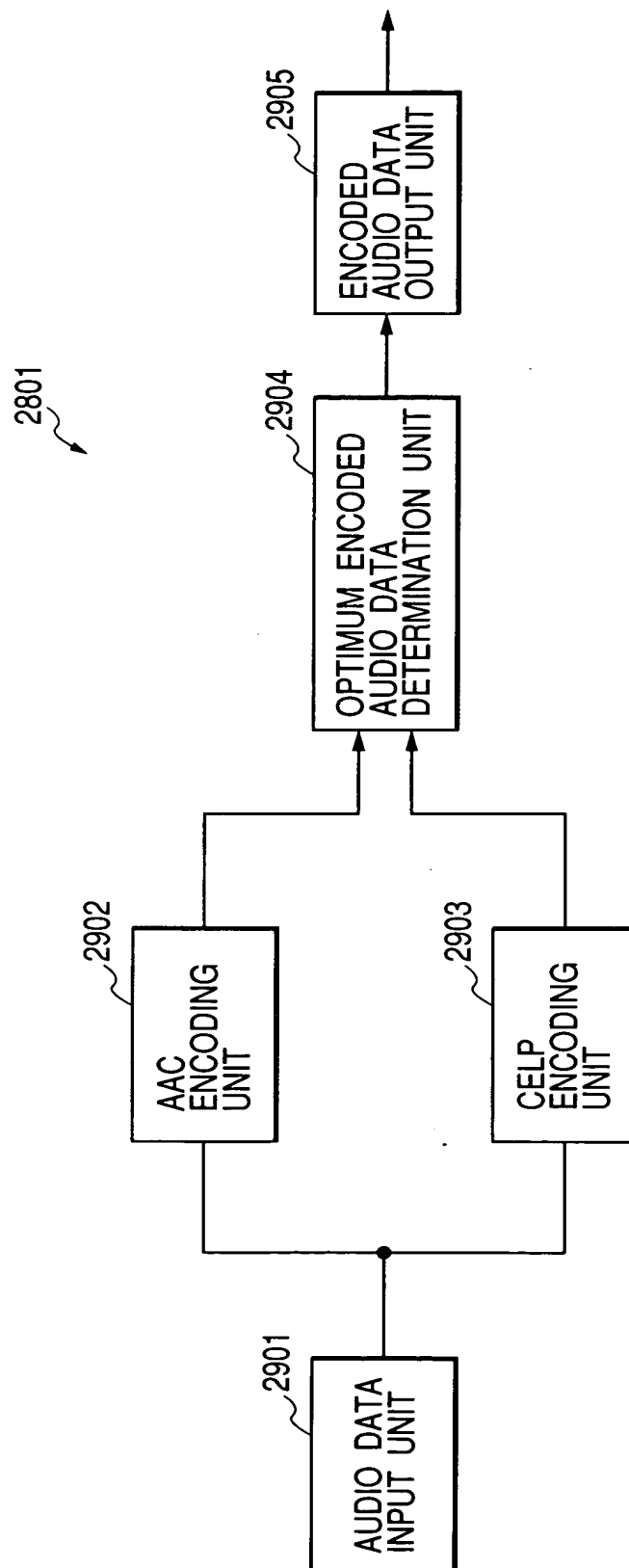


FIG. 30

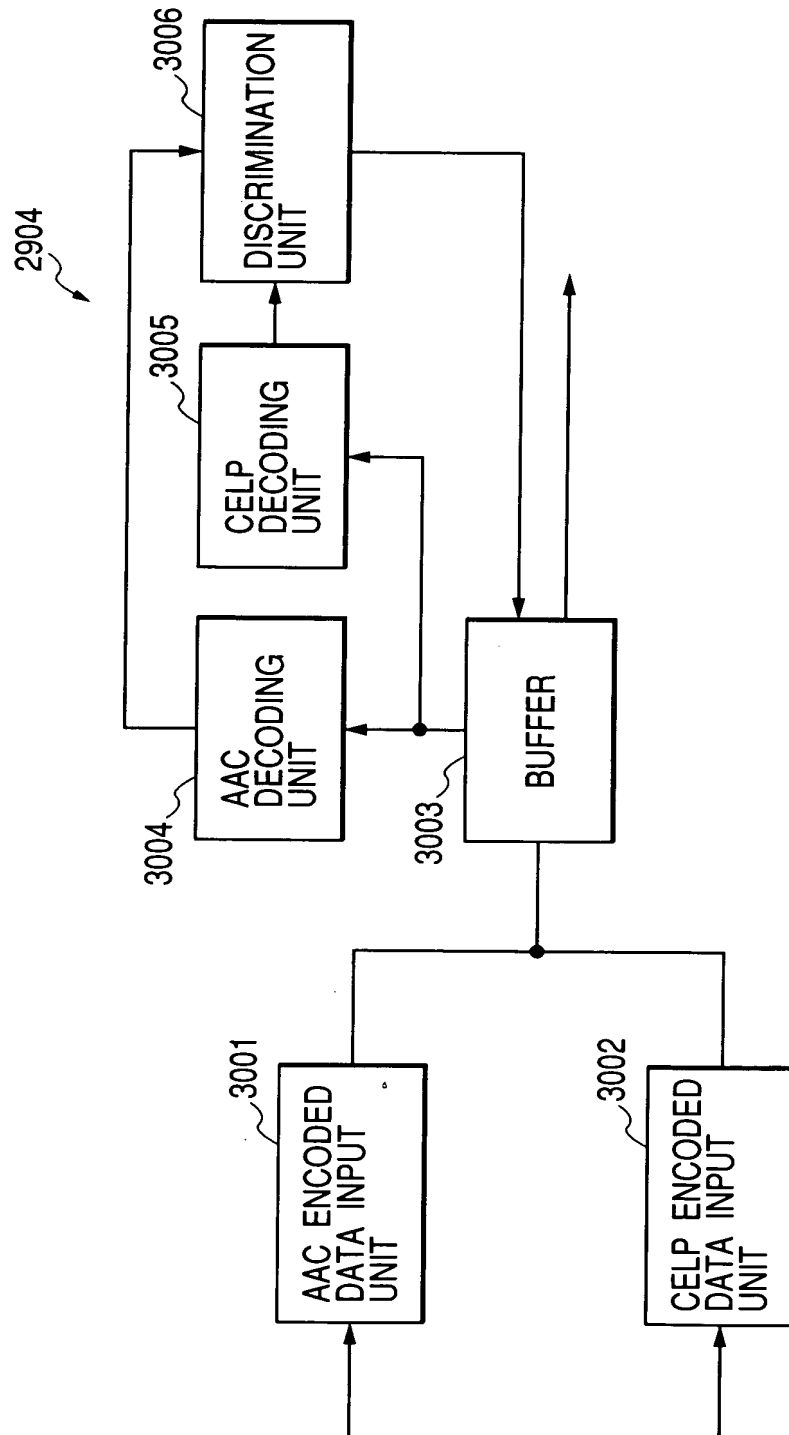


FIG. 31

